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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Nakagawa, K.

Serial No.: 09/688,203

Group Art Unit: 2823

Filed: October 16, 2000

Examiner: Brewster, William

For: METHOD OF MANUFACTURING SEMICONDUCTOR MEMORY DEVICE

Commissioner for Patents
Alexandria, VA 22313-1450

**REQUEST FOR CONSIDERATION OF
INFORMATION DISCLOSURE STATEMENT**

Sir:

Applicant respectfully requests that the Information Disclosure Statement submitted and filed on June 11, 2003, be officially considered of record. Attached is a copy of the Form PTO-1449 filed on June 11, 2003. Applicant respectfully requests that the Examiner officially acknowledge the disclosed information and promptly return the initialed Form PTO-1449.

Dated: 10/23/03

Respectfully submitted,

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OCT 23 2003

ATTY DOCKET NO.

NEC00P264-ks

SERIAL NO.

09/688,203

APPLICANT(S)

Nakagawa

FILING DATE

October 16, 2000

GROUP	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40	41	42	43	44	45	46	47	48	49	50	51	52	53	54	55	56	57	58	59	60	61	62	63	64	65	66	67	68	69	70	71	72	73	74	75	76	77	78	79	80	81	82	83	84	85	86	87	88	89	90	91	92	93	94	95	96	97	98	99	100
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2823

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		08-148658	06/07/96	JAPAN			ABS	
		02-151073	06/11/90	JAPAN			ABS	
		10-326881	12/08/98	JAPAN			ABS	
		08-046159	02/16/96	JAPAN			ABS	
		11-233652	08/27/99	JAPAN			ABS	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

			Japanese Office Action dated March 12, 2003 with partial English translation.

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.